

2827.

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Akihiko EBINA et al. Group Art Unit: 2829

Application No.: 10/052,255 Examiner: S. Geyer

Filed: January 23, 2002 Docket No.: 111778

For: METHOD OF MANUFACTURING SEMICONDUCTOR INTEGRATED CIRCUIT

DEVICE INCLUDING NONVOLATILE SEMICONDUCTOR MEMORY DEVICES

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Pursuant to 37 CFR §1.56, the attention of the Patent and Trademark Office is hereby directed to the reference(s) listed on the attached PTO-1449. Unless otherwise indicated herein, one copy of each reference is attached. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the reference(s) be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

- 1. This Information Disclosure Statement is being filed (a) within three months of the U.S. filing date of this non-CPA application, OR (b) before the mailing date of a first Office Action on the merits in the present application. No certification or fee is required.
- 2. Each item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart application, and this communication was not received by any individual designated in §1.56(c) more than thirty days prior to the filing of this Information Disclosure Statement.

English-language Abstracts of the non-English language references are attached hereto.

Respectfully submitted,

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Date: July 29, 2003

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Sheet 1 of 1

Form PTO-144 (REV. 8-83)	9 US Dept. of Commerce PATENT & TRADEMARK OFFICE		ATTY DO	OCKET NO.		APPLICATION NO. 10/052,255		
INFORMATION DISCLOSURE STATEMENT								
(Use several sheets if necessary)				APPLICANT(S) Akihiko EBINA et al.				
				DATE 3, 2002		GROUP 2829		
	U.S. P	ATEN	NT DOCU	MENTS				
EXAMINER INITIAL	DOCUMENT NUMBER	DATE		NAME	NAME CLASS		SUB CLASS	
	5,408,115	04/18/1995		Chang				
	2003/0053345 A1			Moriya et al.				
	5,891,775	04/06	/1999	Hisamune				
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	FOREIGN	NPAI	ATENT DOCUMENTS				SUB	
	DOCUMENT NUMBER		ATE	COUNTRY		CLASS	CLASS	
	JP 05-326976 (w/Abstract)	10/1993		Japan				
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	OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)							
	European Search Report (EPC Application No. 02002105.1), dated July 11, 2003.							
	Hayashi et al. "Twin Monos Cell With Papers, June 13, 2000, pages 122-123.	Hayashi et al. "Twin Monos Cell With Dual Control Gates", 2000 Symposium on VLSI Technology, Digest of Technical Papers, June 13, 2000, pages 122-123.						
EXAMINER	EXAMINER					DATE CONSIDERED		
Examiner:	Initial if citation considered, whether or not cital conformance and not considered. Include copy of					e through citati	on if not in	

Date: <u>July 29, 2003</u>